

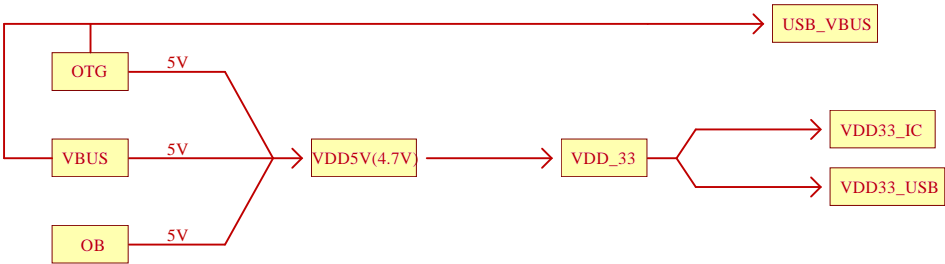
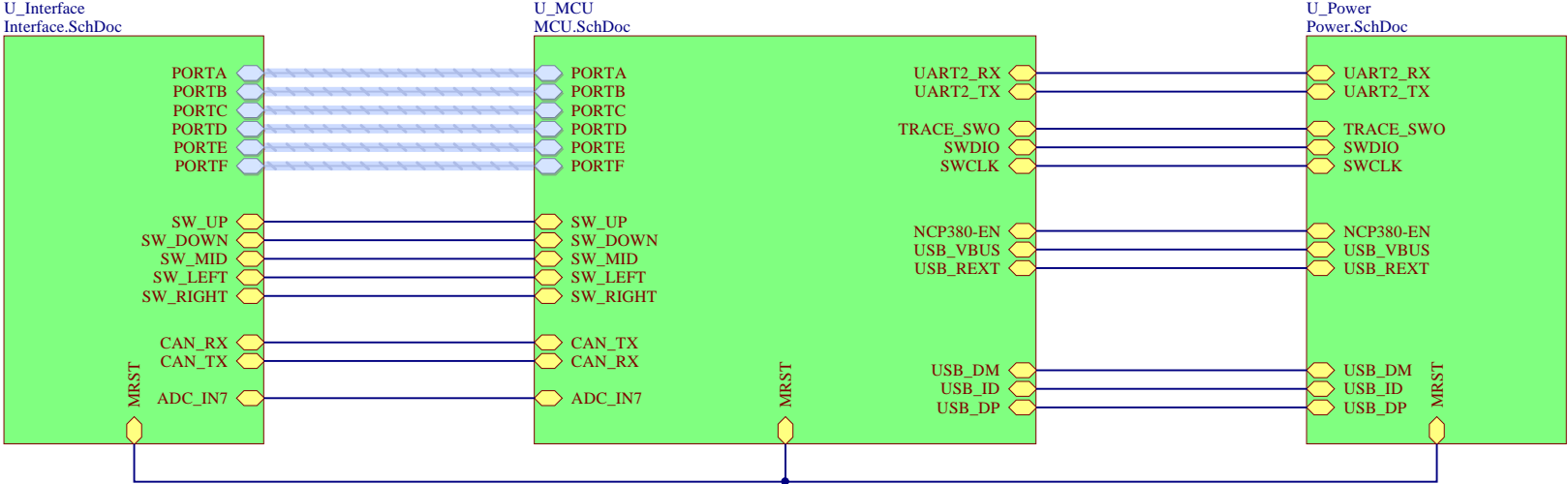
ES-PDS-ES32F3696LX-V1.1.PrjPcb

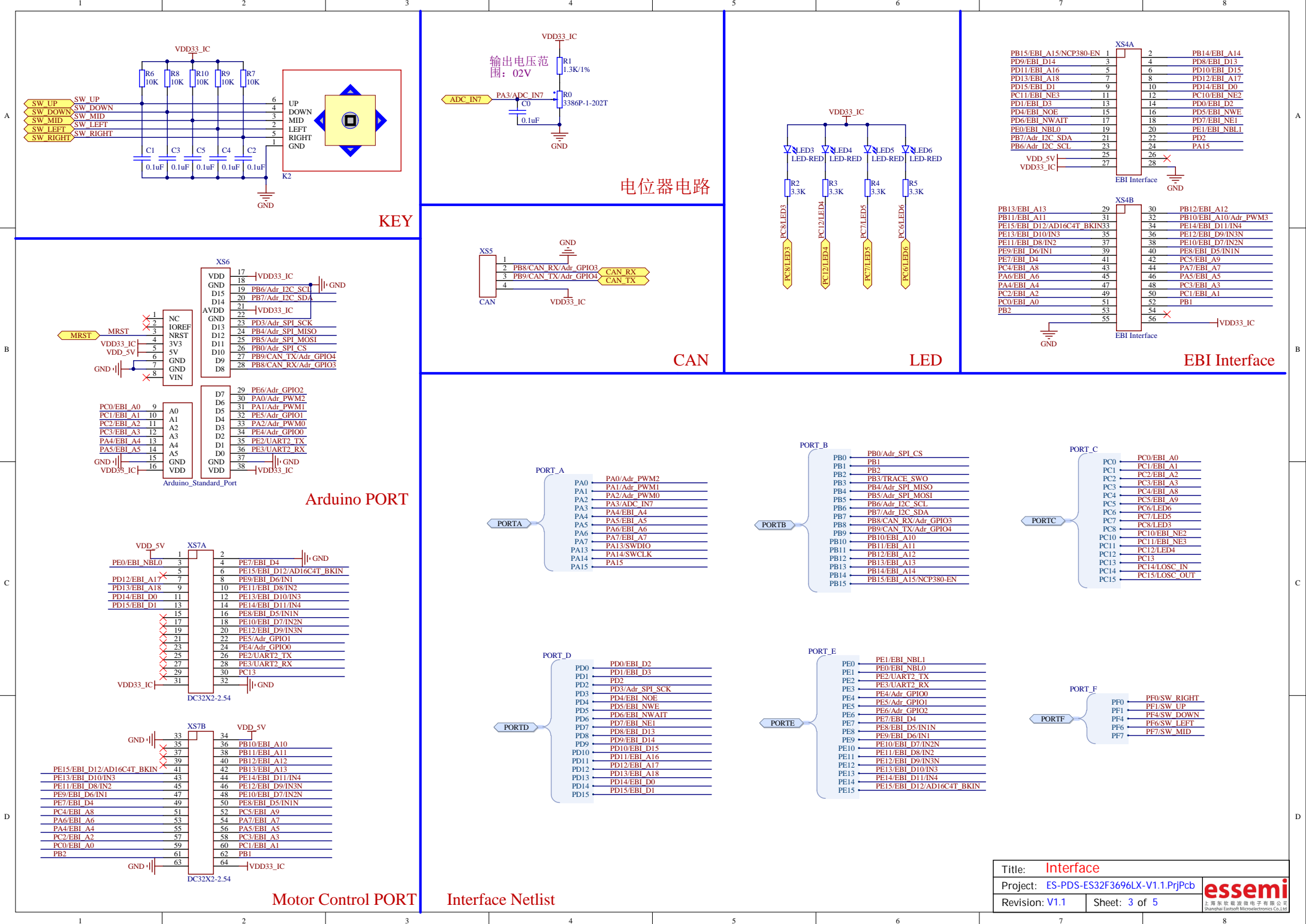
版本记录

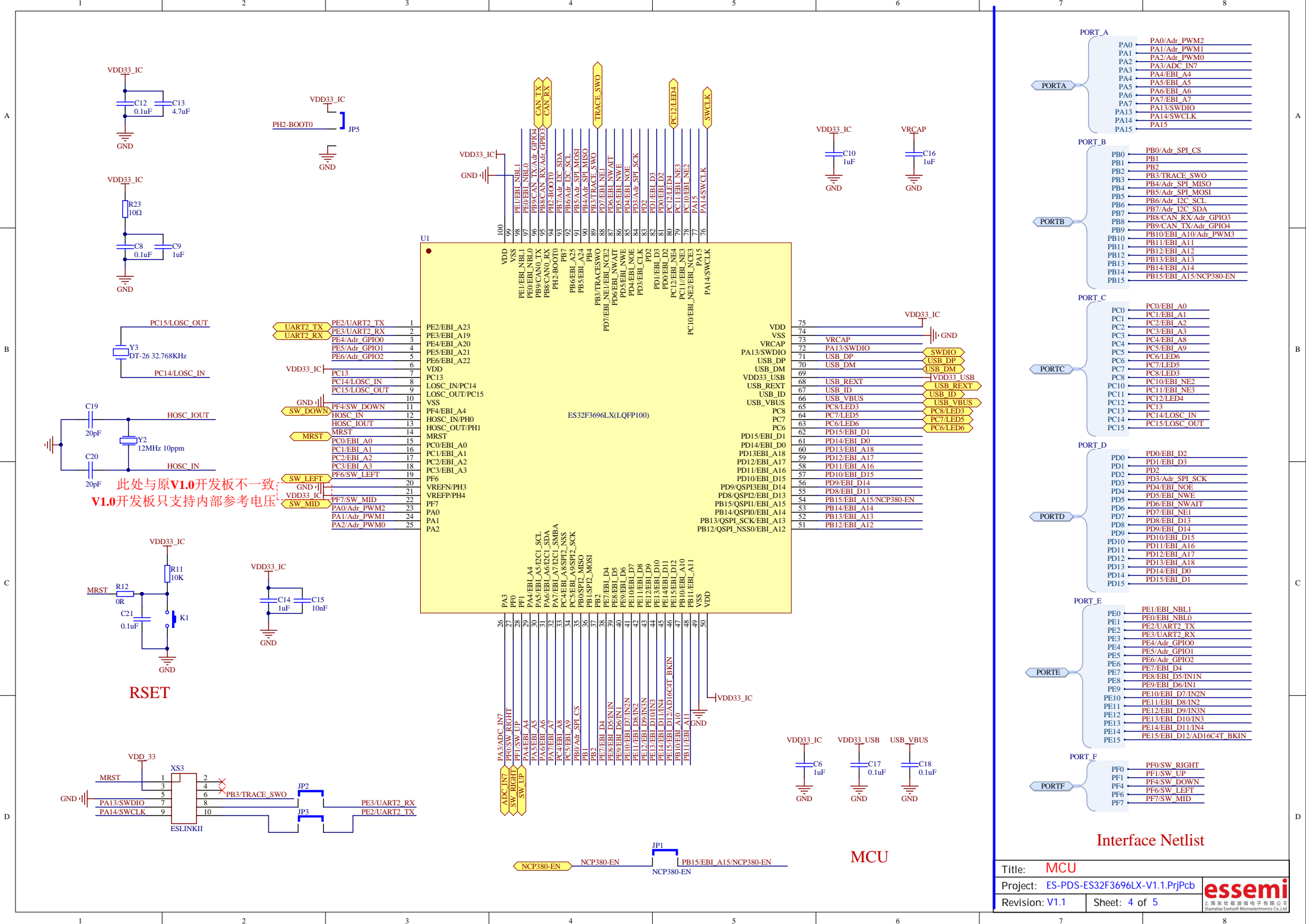
V1.0: 正式1版

V1.1: 正式2版——SCH修改: 1) MCU.SchDoc的U1的VREFN与VREFP功能互换, 已标注与PCB的V1.0版本不同处; 2) 最小系统电路修改到与应用笔记AN2018一致; 3) 原理图布局改动。
PCB修改: 1) USB部分走线调整; 2) 原理图部分对应改动落实。

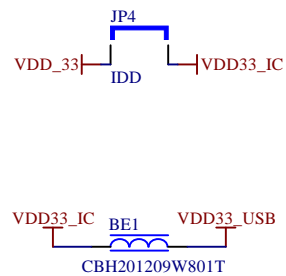
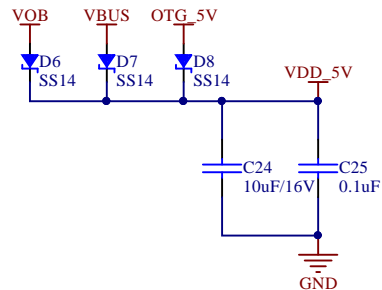
Title: Cover			essemi 上海东软载波微电子有限公司 Shanghai Eastsoft Microelectronics Co., Ltd.
Project: ES-PDS-ES32F3696LX-V1.1.PrjPcb		Revision: V1.1	
Revision: V1.1	Sheet: 1 of 5		



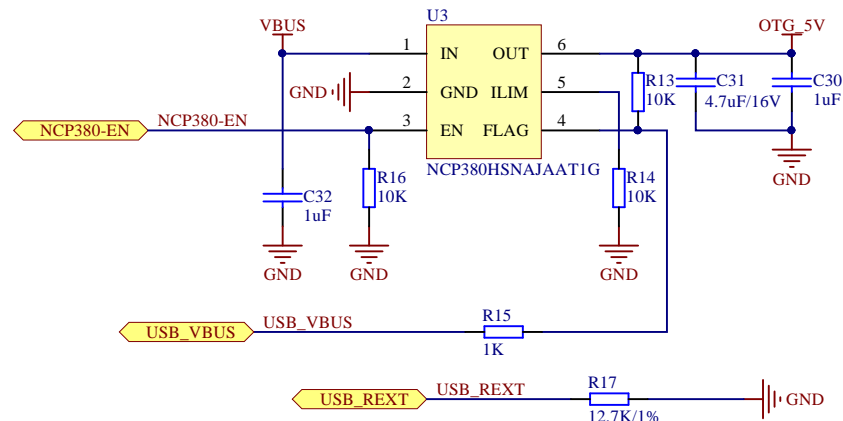




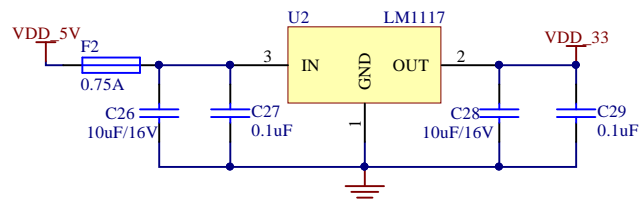
Interface Netlist



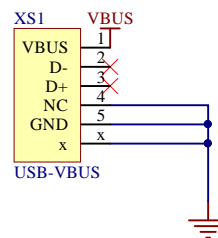
Power_Switch



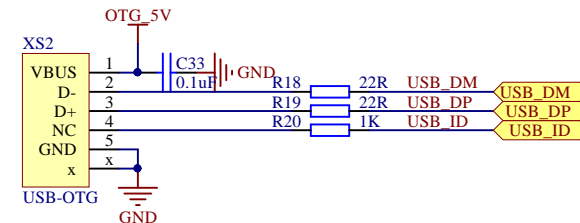
OTG_Power_Switch



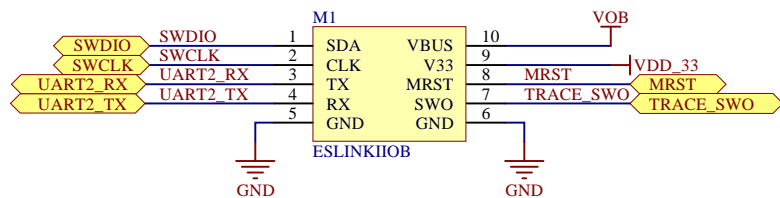
LDO



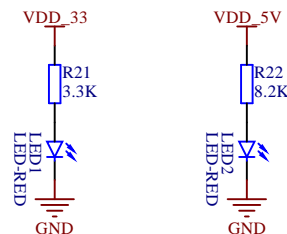
Micro-USB



USB-OTG



DAP-Link



Power_LED